

## Plugin Support In Xilinx Lab Tools

### Applications

Tool - Operation	<a href="#">Xilinx Platform Cable USB II</a>	<a href="#">Xilinx Platform Cable USB</a>	<a href="#">Xilinx Parallel Cable IV</a>	Digilent JTAG Cables	<a href="#">ByteTools EJ-1</a>
<a href="#">ChipScope</a> - FPGA debugging via JTAG	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
<a href="#">ChipScope</a> - FPGA direct programming via JTAG	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
<a href="#">iMPACT</a> - FPGA direct programming via JTAG	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
<a href="#">iMPACT</a> - FPGA direct verify via JTAG	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
<a href="#">iMPACT</a> - FPGA battery-backed RAM programming via JTAG	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
<a href="#">iMPACT</a> - FPGA eFUSE programming via JTAG	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/> ①	<input checked="" type="checkbox"/> ①	<input checked="" type="checkbox"/> ①	<input checked="" type="checkbox"/> ①
<a href="#">iMPACT</a> - Indirect SPI Flash programming via JTAG	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
<a href="#">iMPACT</a> - Indirect BPI Flash programming via JTAG	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
<a href="#">iMPACT</a> - CoolRunner/CoolRunner-II programming via JTAG	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
<a href="#">iMPACT</a> - Xilinx Platform Flash PROM programming via JTAG	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
<a href="#">iMPACT</a> - XC9500/XC9500XL CPLD programming via JTAG	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/> ②	<input checked="" type="checkbox"/> ②
<a href="#">iMPACT</a> - XC1800 PROM programming via JTAG	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/> ③	<input checked="" type="checkbox"/> ③
<a href="#">iMPACT</a> - Debug JTAG chain operation	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/> ④	<input checked="" type="checkbox"/> ④
<a href="#">System Generator</a>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Not tested	Not tested
<a href="#">EDK/XMD</a>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Not tested

#### Notes:

1 - eFUSE programming requires accurate hardware timer implemented in the cable hardware/firmware.

2 - XC9500 CPLD programming requires special functions implemented by the plugin.

3 - XC1800 PROM programming requires accurate hardware timer implemented in the cable hardware/firmware.

4 - Debug Chain operation requires the plugin to support individually setting and reading of JTAG pins.

